

SELECTIVE HARMONIC ELIMINATION ON A MULTILEVEL INVERTER USING ANN AND GENETIC ALGORITHM OPTIMIZATION

SINDHU A,

Abstract— The multilevel inverter is an electronic device which converts DC to AC used for high power utility applications. But the inverter output waveform contains harmonics that has to be eliminated to give sinusoidal waveform with low harmonics less than 3%. This paper presents the selective harmonic elimination in voltage output of 11-level cascaded multilevel inverter by considering varying dc sources. Genetic algorithm (GA) is used to obtain the switching angles and artificial neural network (ANN) is trained to determine the switching angles in an 11-level cascaded multilevel inverter. This proposed method can be applied even when the number of switching angles is increased. This results show the elimination of harmonics.

Index Terms— Multilevel inverter, Cascaded multilevel inverter ANN, Genetic algorithm, Roulette wheel selection, mutation, crossover.

1 INTRODUCTION

Multilevel inverter (MLI) has got vast applications in industrial drives, reactive power compensation, selective harmonic compensator etc. it also have got a high power rating and high quality output waveforms. Among different types of multilevel inverter, cascaded multilevel inverter is used. Cascaded multilevel inverter would provide a possibility to connect a high separate DC source for getting high output voltages which do not exceed the limits. Fast Fourier transforms (FFT) based extraction is used in the MLI voltage waveform. Harmonic pattern of the waveform during the fault condition is used as a feature and neural network is trained using these features. Techniques like Particle swarm optimization (PSO) and Genetic algorithm (GA) used for training neural network is to reduce the errors and time taken for training. An approach to determine optimum switching angles for unequal DC sources is to calculate switching angles off-line and store the solutions in the lookup table. As the levels increases, requirement increases. Problems may occur and this leads to result in which solution becomes bigger. For some operating points, solutions might be missing and hence lookup table is replaced by artificial neural network (ANN) which, if well trained has the capacity to generalize the solutions. Since ANN runs fast, it is possible to quickly determine switching angles to establish real-time control.

Department of EEE, The oxford college of engineering, Bangalore, India. E-mail: sindhu14a.reddy@mail.com

2 PROPOSED SYSTEM

2.1 MULTILEVEL INVERTER (MLI)

Multilevel inverter is like an inverter and it is used for industrial applications as alternative in high power and medium voltage situations.

TYPES OF MLI

It consists of three types

1. Diode clamped multilevel inverter
2. Flying capacitors multilevel inverter
3. Cascaded H-bridge multilevel inverter

2.2 COMPARISION BETWEEN DIFFERENT TYPES OF MULTILEVEL INVERTER

DIODE CLAMPED MLI	FLYING CAPACITORS MLI	CASCADED H-BRIDGE MLI
Its concept is to use diodes and provides multiple voltage level through different phases to capacitor banks, which are in series.	Its concept is to use capacitors and transfer the limited amount of voltage to electrical devices.	Its concept is to use capacitors and switches. Combination of switch pairs and capacitors gives separate input DC voltage.
It requires more components.	It requires more components.	It requires fewer components.
Output is half of the input DC.	Output is half of the input DC.	Output is independent of the input.
Switching losses will take place slightly.	Switching losses are more.	Switching losses will not occur.

This needs bulky transformer.	This also needs bulky transformer.	Eliminates bulky transformer, clamping diodes, flying capacitors.
-------------------------------	------------------------------------	---

By comparing the three MLIs, cascaded H-bridge multilevel inverter is used because of its vast applications and advantages.

2.3 CASCADED H-BRIDGE MULTILEVEL INVERTER

Structure of an 11-level cascaded multilevel inverter is shown in fig (a). Each separate dc sources is connected to H-bridge inverter. Each inverter level can generate three different output voltages, +V dc, 0, and -V dc by connecting the dc source to the AC output by different combinations of the four switches Q11, Q12, Q13, and Q14. Switches Q11 and Q14 are turned on to obtain +V dc. Switches Q12 and Q13 are turned on to obtain -V dc. To obtain 0 voltages switches Q11, Q12 or Q13, Q14 are turned on. The ac outputs of each of the different full-bridge inverter levels are connected in series such that the synthesized voltage waveform is the sum of the inverter outputs. The number of output voltage level m in a cascaded inverter is defined by $m=2s+1$, where s is the number of dc sources.

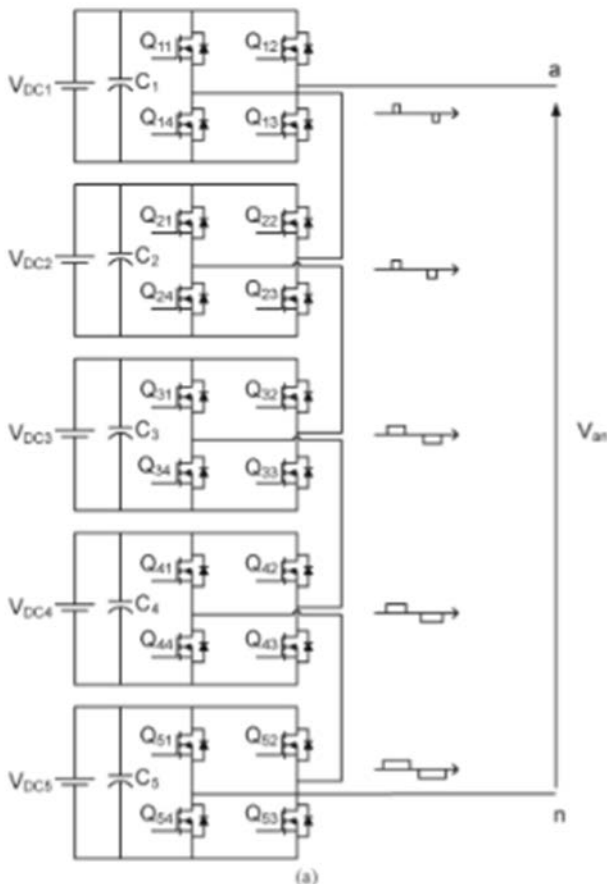


Fig (a). Eleven level cascaded inverter

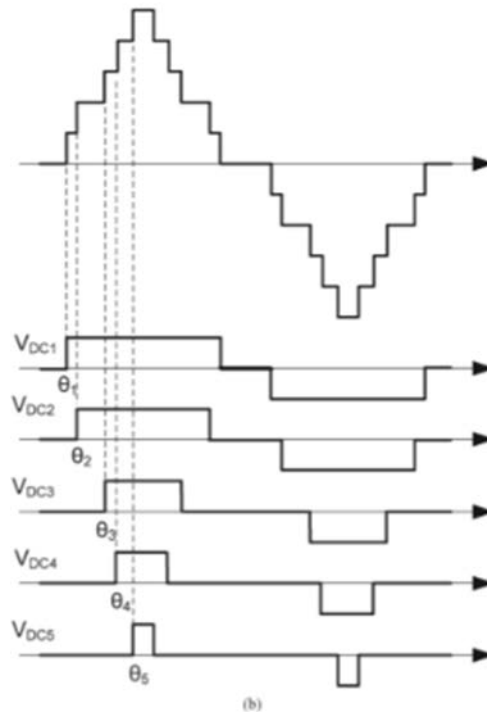


Fig (b). Voltage output waveform

3 GENETIC ALGORITHM

Genetic algorithms are inspired by Darwin's theory about evolution, such as inheritance, mutation, selection and crossover. It is a population based search method used in computing to find true solutions to optimization and search problems.

FEATURES

1. It is a population based
2. Uses recombination to mix information of candidate solutions into a new one
3. It is a stochastic

Genetic algorithm starts with a set of solutions called population (chromosomes). Solution from that population are taken according to their fitness and used to form a new solution hoping that the new solution will be better than the old one. This is repeated until we get the best solution.

Selection: Solutions are selected though fitness based process, where fitter solutions are typically more likely to be selected. This is done according to roulette wheel selection. i.e., individuals are given a probability of being selected that is directly proportional to their fitness.

Crossover: Crossover the parents to form a new offspring. If crossover was not performed, offspring is an exact copy of parents.

Mutation: After selection and crossover, process will have a new population, some are copied and others are produced by crossover. In order to confirm that individuals are not same, mutation is performed.

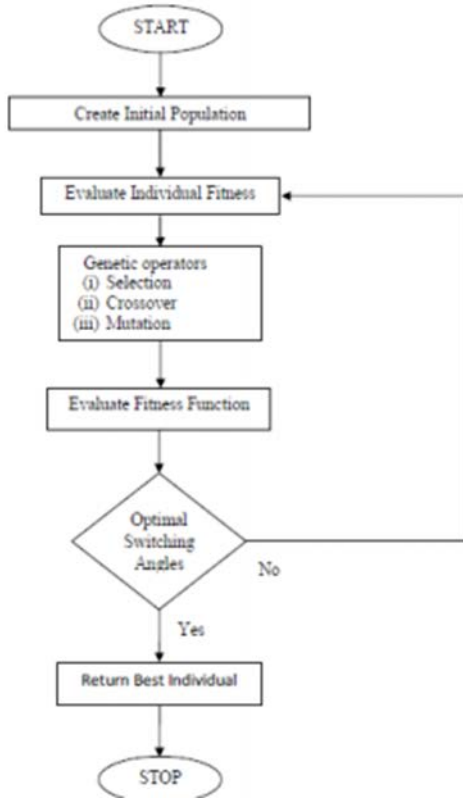


Fig (c). Flowchart of Genetic algorithm

The output equation of waveform shown in fig (b) can be expressed in the Fourier form as

$$V_{an}(wt) = \sum_{n=1,3,5,7,\dots}^{\infty} \frac{4}{\pi \cdot n} (V_{dc1} \cos(n \cdot \theta_1) + V_{dc2} \cos(n \cdot \theta_2) + V_{dc3} \cos(n \cdot \theta_3) + V_{dc4} \cos(n \cdot \theta_4) + V_{dc5} \cos(n \cdot \theta_5)) \cdot \sin(nwt) \dots \dots \dots (1)$$

Where

$V_{dc1} \dots V_{dc5}$ input dc sources;

$\theta_1 \dots \theta_5$ inverter switching angles;

V_{an} inverter output voltage.

Equation (1) shows the contents of the output voltage at infinite frequencies. For different DC sources, it is necessary to maintain the fundamental output voltage. The set of equations for the genetic algorithm is

$$V_{fund} = \frac{4}{\pi} \cdot (V_{dc1} \cos(\theta_1) + V_{dc2} \cos(\theta_2) + \dots + V_{dc5} \cos(\theta_5)) \dots \dots \dots$$

$$(2)$$

$$V_{5th} = \frac{4}{\pi \cdot 5} \cdot (V_{dc1} \cos(5\theta_1) + V_{dc2} \cos(5\theta_2) + \dots + V_{dc5} \cos(5\theta_5)) \dots (3)$$

$$V_{7th} = \frac{4}{\pi \cdot 7} \cdot (V_{dc1} \cos(7\theta_1) + V_{dc2} \cos(7\theta_2) + \dots + V_{dc5} \cos(7\theta_5)) \dots (4)$$

$$V_{11th} = \frac{4}{\pi \cdot 11} \cdot (V_{dc1} \cos(11\theta_1) + V_{dc2} \cos(11\theta_2) + \dots + V_{dc5} \cos(11\theta_5)) \dots (5)$$

$$V_{13th} = \frac{4}{\pi \cdot 13} \cdot (V_{dc1} \cos(13\theta_1) + V_{dc2} \cos(13\theta_2) + \dots + V_{dc5} \cos(13\theta_5)) \dots (6)$$

Set of switching angles are obtained from genetic algorithms to control the multilevel inverter for each value of dc sources using (2) - (6).

It is necessary to have the real DC source values and the output voltage to the above equations using GA. After measuring the real values of the DC sources, set of switching angles is found so that the output voltage is kept constant and the 5th, 7th, 11th and 13th harmonics are eliminated. An objective function for the GA that evaluates and classifies each individual in the population was defined by the equation,

$$f(V_{fund}, V_{5th}, V_{7th}, V_{11th}) = k_1 |V_{fund} - 110| + k_2 |V_{5th}| + k_3 |V_{7th}| + k_4 |V_{11th}| + k_5 |V_{13th}| \dots (7)$$

In the above equation, coefficient k_1 should have lesser value than the coefficients k_2 to k_5 . Assume $k_2=k_3=k_4=k_5=100$ and $k_1=10$. Range from 24 to 40 V is defined for each DC source with a step of 1V for the first source.

4 ARTIFICIAL NEURAL NETWORK

Obtaining the switching angles is limited to equal dc sources. But in this paper we can obtain the switching angles for unequal dc sources and solutions will be stored in look up table. But with the look up table some operating points might be missed. Hence instead of look up table artificial neural network is used to store the solutions, which if well trained has the capacity to solve the solutions which are complex in nature. With the ANN process will be very fast and gives the results quickly.

TABLE 1. Data set obtained from GA run

Input voltages (V)	Output angles (°)
25 25 25 25 25	0.6 10.2 18.3 11 38.2
26 27 29 30 32	6.0 17.3 21.6 29.7 32.5
...
33 35 37 38 39	8.2 30.1 32.9 78.1 81.0
.....
40 40 40 40 40	22.1 39.152.9 6.5 73.2

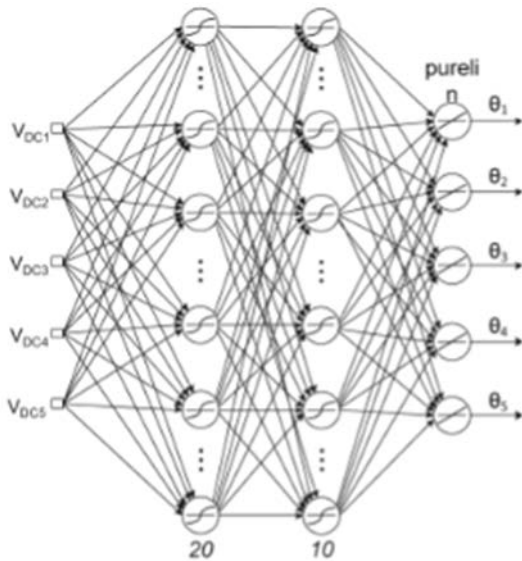


Fig (d). Feedforward ANN topology

5 RESULTS

The experimental results for an 11-level inverter operating with different dc sources are shown with the voltage values indicated. In Fig (f), the frequency spectrum is shown where it can be noticed that the aim harmonics were minimized to less than 1% with the exception of the 13th harmonic that is around 1.2% with a THD of 8.7%. In this same figure, a high value of the third and ninth harmonics can be noticed; those harmonics were not minimized due to the fact that they will be canceled in line voltage for a three-phase application.

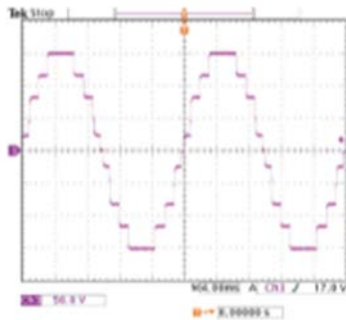


Fig (e). Voltage output waveform

This system has the capacity to modulate the angles in real time at speeds higher than the low switching frequency (1/60 s), but the angle speed update is done at the end of a

cycle of the low frequency to avoid even harmonics. It is assumed that a substantial step variation in the magnitude of the dc source inputs may occur for this approach, so that the system should be able to adapt its output.

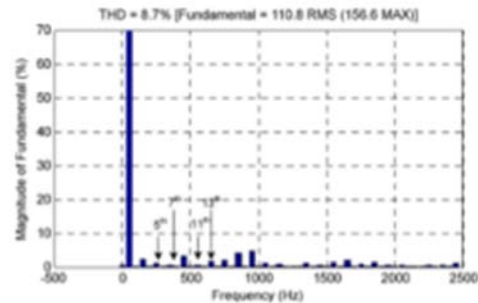


Fig (f). Frequency spectrum

6 CONCLUSION

A new proposal for real-time computation of switching angles using ANNs has been described. This method is very efficient and reliable. The solutions were found offline using GA to obtain a data set for use during the training process of the neural network and to explore the advantages of approximate solutions obtained by the GA. The tutored neural network is used then for online real-time determination of the angles.

ACKNOWLEDGEMENT

I am very thankful to my guide Mrs. Devi Vighneshwari, asst professor, The oxford college of engineering and also Dr.H.B.Phani Raju, HOD of electrical dept, The oxford college of engineering, Bangalore for their support and guidance.

REFERENCES

- [1] FaeteFilho, HelderZandonadi Maia, Tiago H. A. Mateus, BurakOzpineci, Leon M. Tolbert, and João O. P. Pinto., "Adaptive Selective Harmonic Minimization Based on ANNs for Cascade Multilevel Inverters with Varying dc Sources", IEEE Transactions on Industrial Electronics, Vol. 60, No. 5, pp.1955-1962, May 2013
- [2] Sudhakar V. Pawar1, Mrs. Shimi S.L.2 M.E. (I&C) Student, Electrical Department, NITTTR, Chandigarh, India Asst. Professor, Electrical Department, NITTTR, Chandigarh, India
- [3] Faete Filho, Leon M. Tolbert, Yue Cao Electrical Engineering and Computer Science The University of Tennessee Knoxville, Tennessee USA, Burak Ozpineci Oak Ridge National Laboratory Oak Ridge, Tennessee USA

[4] K Rama Chakravarthy, SKGouse Basha Pursuing M.Tech in the field of Power Electronics, NCET, Vijayawada, AP, India Working as, Assistant professor in the EEE Dept, NCET, Vijayawada, AP, India

[5] J. Napoles, J. I. Leon, R. Portillo, L. G. Franquelo, and M. A. Aguirre, "Selective harmonic mitigation technique for high-power converters," *IEEE Trans. Ind. Electron.*, vol. 57, no. 7, pp. 2315–2323, Jul. 2010.

[6] J. Chavarria, D. Biel, F. Guinjoan, C. Meza, and J. J. Negroni, "Energy balance control of PV cascaded multilevel grid-connected inverters under level-shifted and phase-shifted PWMs," *IEEE Trans. Ind. Electron.*, vol. 60, no. 1, pp. 98–111, Jan. 2013.

[7] B. Ozpineci, L. M. Tolbert, and J. N. Chiasson, "Harmonic optimization of multilevel converters using genetic algorithms," *IEEE Power Electron Lett.*, vol. 3, no. 3, pp. 92–95, Sep. 2005

[8] J. N. Chiasson, L. M. Tolbert, K. J. McKenzie, and Z. Du, "A unified approach to solving the harmonic elimination equations in multilevel converters," *IEEE Trans. Power Electron.*, vol. 19, no. 2, pp. 478–490, Mar. 24.

[9] J. N. Chiasson, L. M. Tolbert, K. J. McKenzie, and Z. Du, "Elimination of harmonics in a multilevel converter using the theory of symmetric polynomials and resultants," *IEEE Trans. Control Syst. Technol.*, vol. 13, no. 2, pp. 216–223, Mar. 2005.

[10] Y. Liu, H. Hong, and A. Q. Huang, "Real-time calculation of switching angles minimizing THD for multilevel inverters with step modulation," *IEEE Trans. Ind. Electron.*, vol. 56, no. 2, pp. 285–293, Feb. 2009.

[11] N. Yousefpoor, S. H. Fathi, N. Farokhnia, and H. A. Abyaneh, "THD minimization applied directly on the line-to-line voltage of multilevel inverters," *IEEE Trans. Ind. Electron.*, vol. 59, no. 1, pp. 373–380, Jan. 2012.

[12] Z. Du, L.M. , J.N. Chiasson, and H. Li, "Low switching frequency active harmonic elimination in multilevel converters with unequal DC voltages," in *Conf. Rec. IEEE IAS Annu. Meeting*, Oct. 2005, vol. , pp. 92–98.

[13] Z. Du, L. M. Tolbert, and J. N. Chiasson, "Active harmonic elimination for multilevel converters," *IEEE Trans. Power Electron.*, vol. 21, no. 2, pp. 459–469, Mar. 2006.

[14] M. G. H. Aghdam, S. H. Fathi, and G. B. Gharehpetian, "Elimination of harmonics in a multi-level inverter with unequal DC sources uses the homotopy algorithm," in *Proc. IEEE Int. Symp. Ind. Electron.* Jun. 2007, pp. 578–583.

[15] J.R. Wells, B.M. Nee, and P.L. Chapman, "Selective harmonic control: A general problem formulation and se-

lected solutions," *IEEE Trans. Power Electron.*, vol. 20, no. 6, pp. 1337–1345, Nov. 2005

[16] M. S. A. Dahidah and V. G. Agelidis, "Selective harmonic elimination PWM control for cascaded multilevel voltage source converters: A generalized formula," *IEEE Trans. Power Electron.*, vol. 23, no. 4, pp. 1620–1630, Jul. 2008.

[17] D. W. Kang, H. C. Kim, T. J. Kim, and D. S. Hyun, "A simple method for acquiring the conducting angle in a multilevel cascaded inverter using step pulse waves," *Proc. Inst. Elect. Eng.—Elect. Power Appl.*, vol. 152, no. 1, pp. 103–111, Jan. 2005.

[18] F. J. T. Filho, T. H. A. Mateus, H. Z. Maia, B. Ozpineci, J. O. P. Pinto, and L. M. Tolbert, "Real-time selective harmonic minimization in cascaded multilevel inverters with varying DC sources," in *Proc. Power Electron. Spec. Conf.*, Jun. 2008, pp. 4302–4306.

